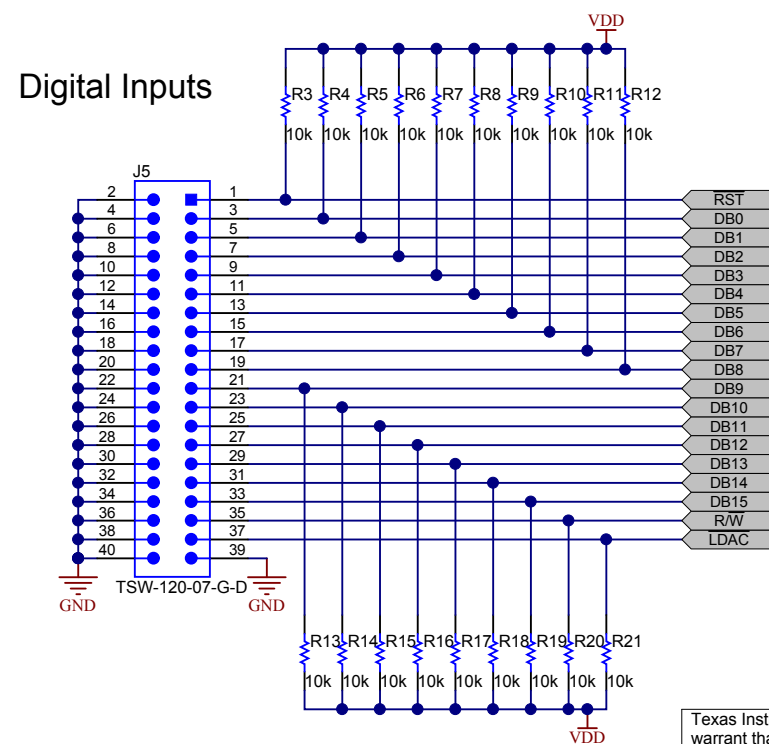
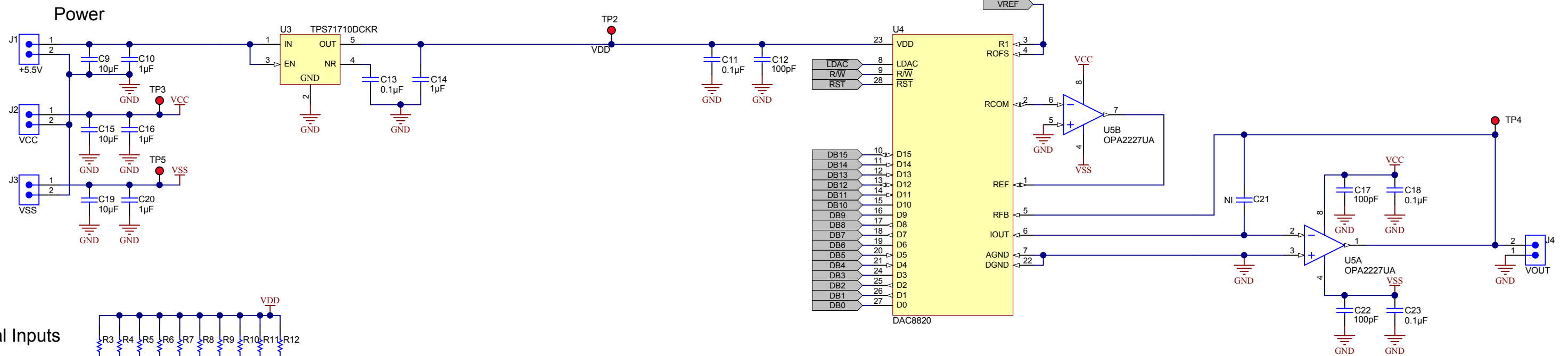
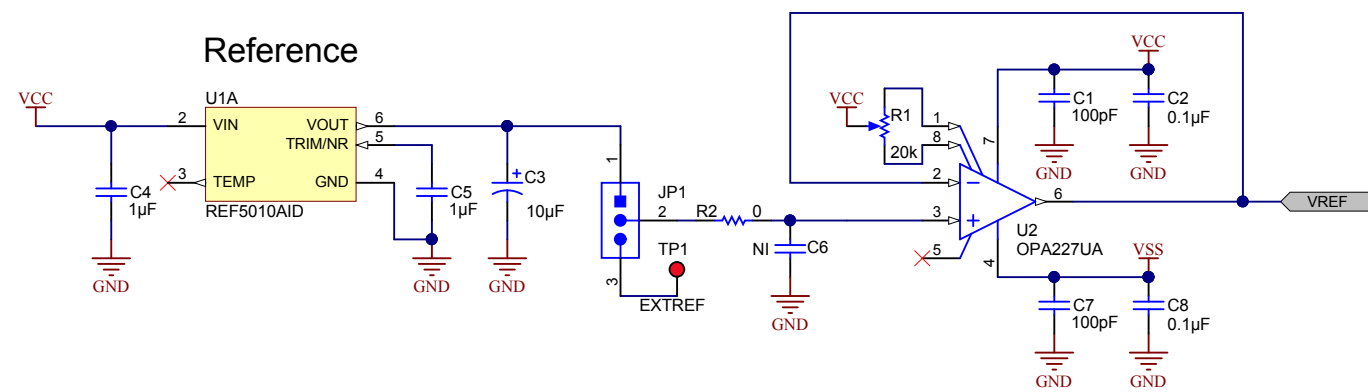



Low-noise Precision Variable Reference



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A	Designed for: Public Release	Mod. Date: 6/4/2015	 TEXAS INSTRUMENTS http://www.ti.com © Texas Instruments 2015
TID #: 194	Project Title: Low-noise Precision Voltage Reference		
Number: TIPD194	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]	Sheet: 2 of 3	
Drawn By:	File: TIPD194.SchDoc	Size: B	
Engineer: Eugenio Mejia	Contact: http://www.ti.com/support		



PCB Number: TIPD194
PCB Rev: E1

PCB
LOGO
Texas Instruments

PCB
LOGO
Pb-Free Symbol

A You should delete the nylon screws/standoffs and/or the bump-ons as needed for your design (or substitute other parts from Hardware.IntLib). Bump-ons are cheaper, but provide less clearance.

Deleting anything else from this page may result in your EVM submission being rejected (until you add them back).

Update the Label Text in the Label Table as needed for each Assembly Variant.

You can delete this note too.

Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!

ZZ1
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ2
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ3
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: N/A		Designed for: Public Release	Mod. Date: 6/4/2015
TID #: 194		Project Title: Low-noise Precision Voltage Reference	
Number: TIPD194	Rev: E1	Sheet Title:	
SVN Rev: Version control disabled	Assembly Variant: [No Variations]		Sheet: 3 of 3
Drawn By:	File: TIPD194_Hardware.SchDoc		Size: B
Engineer: Eugenio Mejia	Contact: http://www.ti.com/support		